

Attorney's Docket No.: 19415-005US1 / PCT-04R-155/US



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hidetoshi Nishikawa

Art Unit : Unknown

Serial No.: 10/561,552

Examiner: Unknown

Filed

: December 20, 2005

Title

: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

MAIL STOP AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form, copies of which are enclosed.

English-language abstracts and machine translations are included for all listed references.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office Action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

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155/US

Respectfully submitted,

Date: 3/20/06

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Sheet	1	of	1

PI	Substitute (Modified	Form PTO-1449	U.S. Department of Patent and Trade
6	a 2006		closure Statement
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U.S. Department of Commerce Patent and Trademark Office

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Application No. 10/561,552

Applicant

Hidetoshi Nishikawa

Filing Date

Group Art Unit

December 20, 2005

U.S. Patent Documents							
Examiner	Desig.	Document	Publication				Filing Date
Initial	ID	Number	Date	Patentee	Class	Subclass	If Appropriate
	AA						
	AB						
	AC	1					
-	AD						
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Foreign Patent Documents or Published Foreign Patent Applications								
Examiner	Desig.	Document	Publication	Country or			Trans	lation
Initial	ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	AL	05-090942	04/09/1993	Japan				
	AM	06-061828	03/04/1994	Japan				
	AN							
	AO							
	AP							

(Other Documents (include Author, Title, Date, and Place of Publication)			
Examiner Initial	Desig. ID	Document		
	AQ			
	AR			
	AS			
	AT			

Examiner Signature	Date Considered
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.